

# BLA6H0912-500

LDMOS avionics radar power transistor

Rev. 05 — 1 September 2015

AMPLEON

Product data sheet

## 1. Product profile

### 1.1 General description

500 W LDMOS power transistor intended for avionics transmitter applications in the 960 MHz to 1215 MHz range such as Mode-S, TCAS, JTIDS, DME and TACAN.

**Table 1. Test information**

Typical RF performance at  $T_{case} = 25\text{ °C}$ ;  $t_p = 128\text{ }\mu\text{s}$ ;  $\delta = 10\%$ ;  $I_{Dq} = 100\text{ mA}$ ; in a class-AB production test circuit.

Mode of operation	f (MHz)	V <sub>DS</sub> (V)	P <sub>L</sub> (W)	G <sub>p</sub> (dB)	$\eta_D$ (%)	t <sub>r</sub> (ns)	t <sub>f</sub> (ns)
pulsed RF	960 to 1200	50	450	17	50	20	6

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

### 1.2 Features and benefits

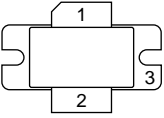
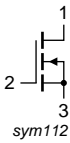
- Typical pulsed RF performance at a frequency of 960 MHz to 1215 MHz, a supply voltage of 50 V, an  $I_{Dq}$  of 100 mA, a  $t_p$  of 128  $\mu\text{s}$  with  $\delta$  of 10 %:
  - ◆ Output power = 450 W
  - ◆ Power gain = 17 dB
  - ◆ Efficiency = 50 %
- Easy power control
- Integrated ESD protection
- High flexibility with respect to pulse formats
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (960 MHz to 1215 MHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

- A-band power amplifiers for radar applications in the 960 MHz to 1215 MHz frequency range

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain		 sym112
2	gate		
3	source		

[1] Connected to flange.

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLA6H0912-500	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT634A

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	100	V
$V_{GS}$	gate-source voltage		-0.5	+13	V
$I_D$	drain current		-	54	A
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		-	200	°C

## 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$Z_{th(j-c)}$	transient thermal impedance from junction to case	$T_{case} = 85\text{ °C}; P_L = 450\text{ W}$		
		$t_p = 32\text{ }\mu\text{s}; \delta = 2\text{ \%}$	0.03	K/W
		$t_p = 128\text{ }\mu\text{s}; \delta = 10\text{ \%}$	0.08	K/W
		$t_p = 2400\text{ }\mu\text{s}; \delta = 6.4\text{ \%}$	0.2	K/W

## 6. Characteristics

**Table 6. DC characteristics**

$T_j = 25\text{ °C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}; I_D = 2.7\text{ mA}$	100	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10\text{ V}; I_D = 270\text{ mA}$	1.3	1.8	2.2	V
$I_{DSS}$	drain leakage current	$V_{GS} = 0\text{ V}; V_{DS} = 50\text{ V}$	-	-	3.6	$\mu\text{A}$
$I_{DSX}$	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; V_{DS} = 10\text{ V}$	53.5	64	-	A
$I_{GSS}$	gate leakage current	$V_{GS} = 11\text{ V}; V_{DS} = 0\text{ V}$	-	-	360	nA
$g_{fs}$	forward transconductance	$V_{DS} = 10\text{ V}; I_D = 405\text{ mA}$	2.50	3.5	4.55	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; I_D = 14.18\text{ A}$	-	70	85	$\text{m}\Omega$

**Table 7. RF characteristics**

Mode of operation: pulsed RF;  $f = 960\text{ MHz}$  to  $1215\text{ MHz}$ ;  $t_p = 128\text{ }\mu\text{s}$ ;  $\delta = 10\%$ ; RF performance at  $V_{DS} = 50\text{ V}$ ;  $I_{Dq} = 100\text{ mA}$ ;  $T_{case} = 25\text{ °C}$ ; unless otherwise specified, in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$P_L$	output power		-	450	-	W
$V_{DS}$	drain-source voltage	$P_L = 450\text{ W}$	-	-	50	V
$G_p$	power gain	$P_L = 450\text{ W}$	16	17	-	dB
$RL_{in}$	input return loss	$P_L = 450\text{ W}$	7	11	-	dB
$\eta_D$	drain efficiency	$P_L = 450\text{ W}$	45	50	-	%
$P_{\text{droop(pulse)}}$	pulse droop power	$P_L = 450\text{ W}$	-	0	0.3	dB
$t_r$	rise time	$P_L = 450\text{ W}$	-	20	50	ns
$t_f$	fall time	$P_L = 450\text{ W}$	-	6	50	ns

### 6.1 Ruggedness in class-AB operation

The BLA6H0912-500 is capable of withstanding a load mismatch corresponding to  $V_{SWR} = 10 : 1$  through all phases under the following conditions:  $f = 960\text{ MHz}$ ,  $1030\text{ MHz}$ ,  $1090\text{ MHz}$  or  $1215\text{ MHz}$ .  $V_{DS} = 50\text{ V}$ ;  $I_{Dq} = 100\text{ mA}$ ;  $P_L = 450\text{ W}$ ;  $t_p = 128\text{ }\mu\text{s}$ ;  $\delta = 10\%$ .

7. Application information

7.1 Impedance information

Table 8. Typical impedance  
 Typical values per section unless otherwise specified.

f MHz	Z <sub>S</sub> Ω	Z <sub>L</sub> Ω
960	1.36 – j1.45	1.49 – j1.48
1030	1.54 – j1.25	1.51 – j1.45
1090	1.67 – j1.22	1.36 – j1.47
1140	1.68 – j1.29	1.15 – j1.41
1215	1.43 – j1.42	0.79 – j1.17

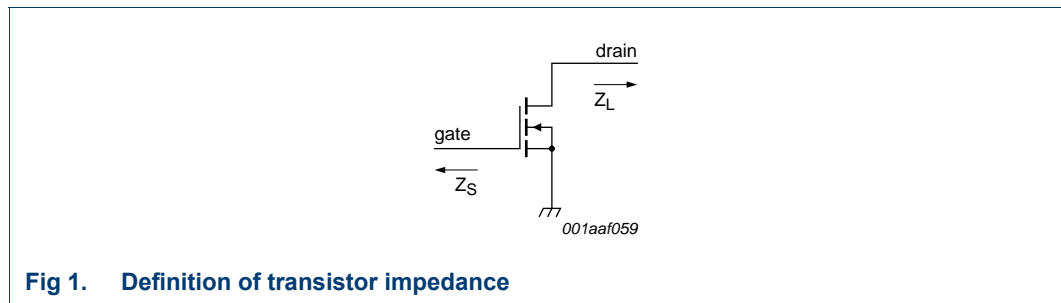
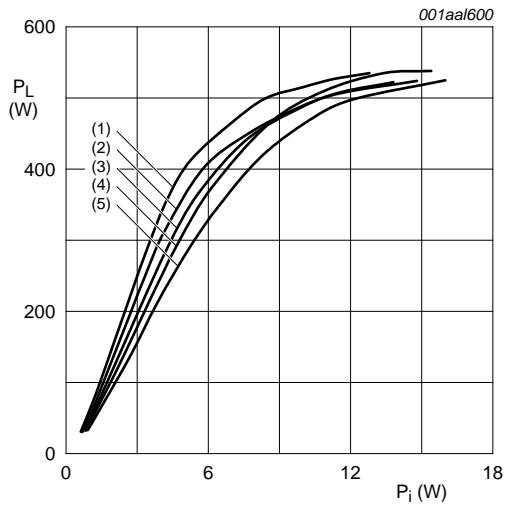


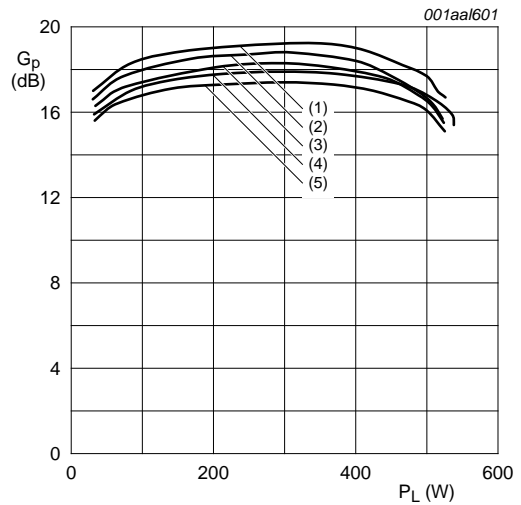
Fig 1. Definition of transistor impedance

7.2 Performance curves



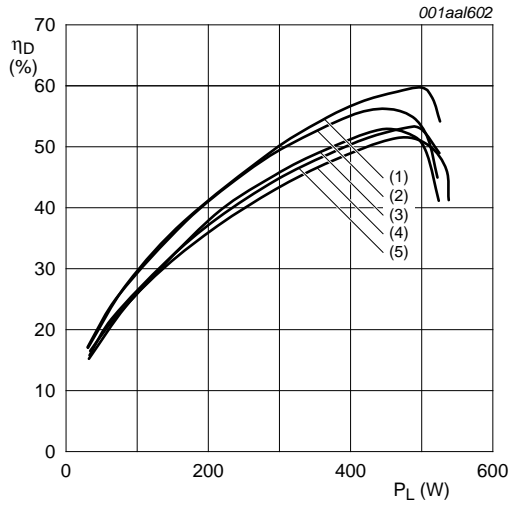
- $V_{DS} = 50 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 128 \text{ }\mu\text{s}; \delta = 10 \text{ \%}.$
- (1)  $f = 960 \text{ MHz}$
  - (2)  $f = 1030 \text{ MHz}$
  - (3)  $f = 1090 \text{ MHz}$
  - (4)  $f = 1140 \text{ MHz}$
  - (5)  $f = 1215 \text{ MHz}$

**Fig 2. Load power as a function of input power; typical values**



- $V_{DS} = 50 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 128 \text{ }\mu\text{s}; \delta = 10 \text{ \%}.$
- (1)  $f = 960 \text{ MHz}$
  - (2)  $f = 1030 \text{ MHz}$
  - (3)  $f = 1090 \text{ MHz}$
  - (4)  $f = 1140 \text{ MHz}$
  - (5)  $f = 1215 \text{ MHz}$

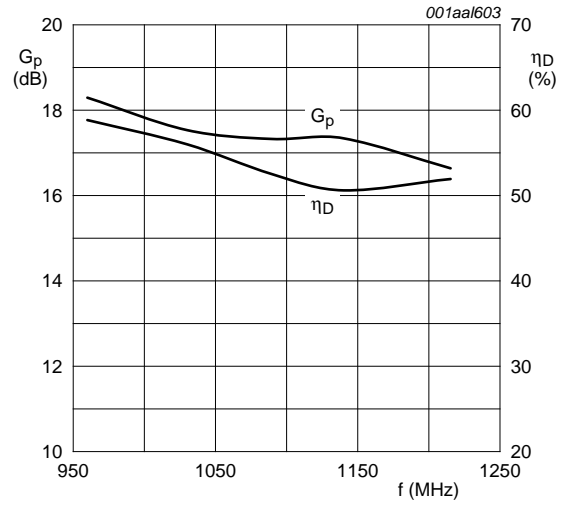
**Fig 3. Power gain as a function of load power; typical values**



$V_{DS} = 50 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 128 \text{ }\mu\text{s}; \delta = 10 \text{ \%}$ .

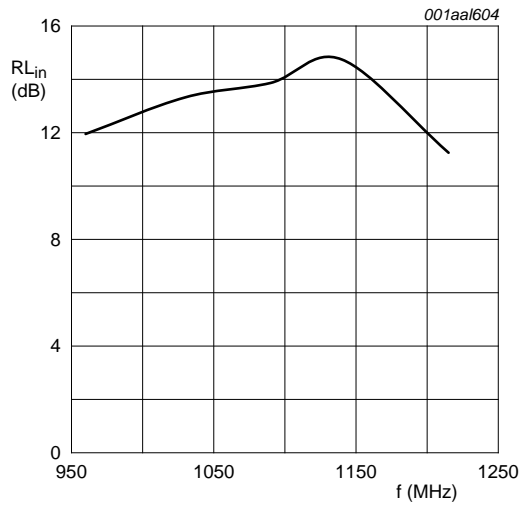
- (1)  $f = 960 \text{ MHz}$
- (2)  $f = 1030 \text{ MHz}$
- (3)  $f = 1090 \text{ MHz}$
- (4)  $f = 1140 \text{ MHz}$
- (5)  $f = 1215 \text{ MHz}$

**Fig 4. Drain efficiency as a function of load power; typical values**



$V_{DS} = 50 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 128 \text{ }\mu\text{s}; \delta = 10 \text{ \%}$ .

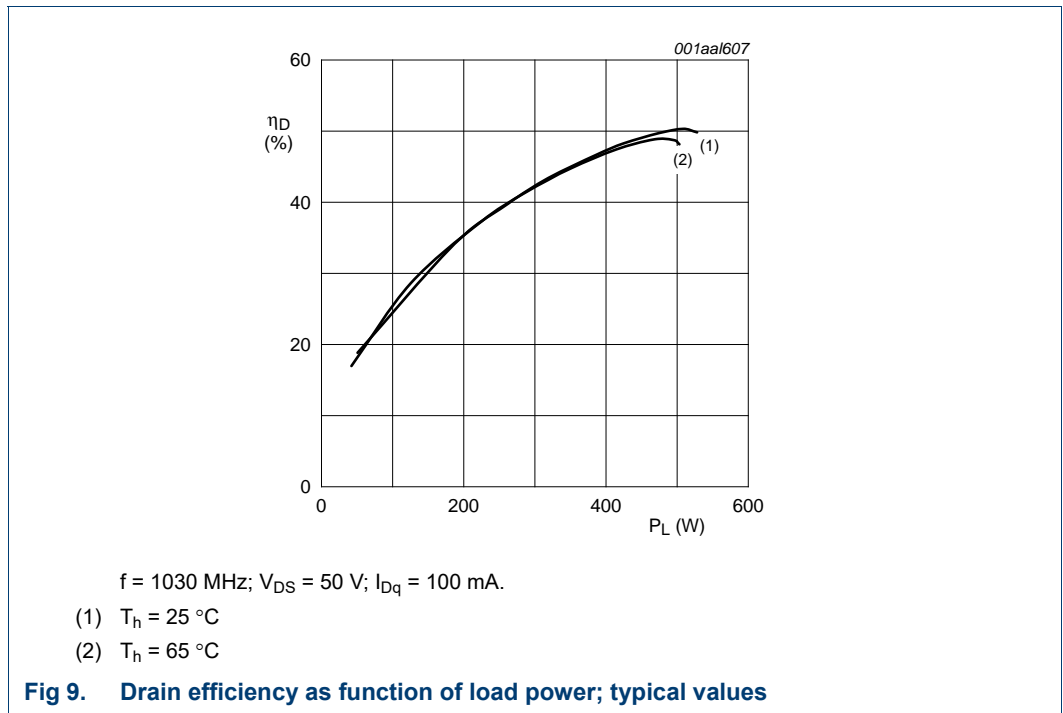
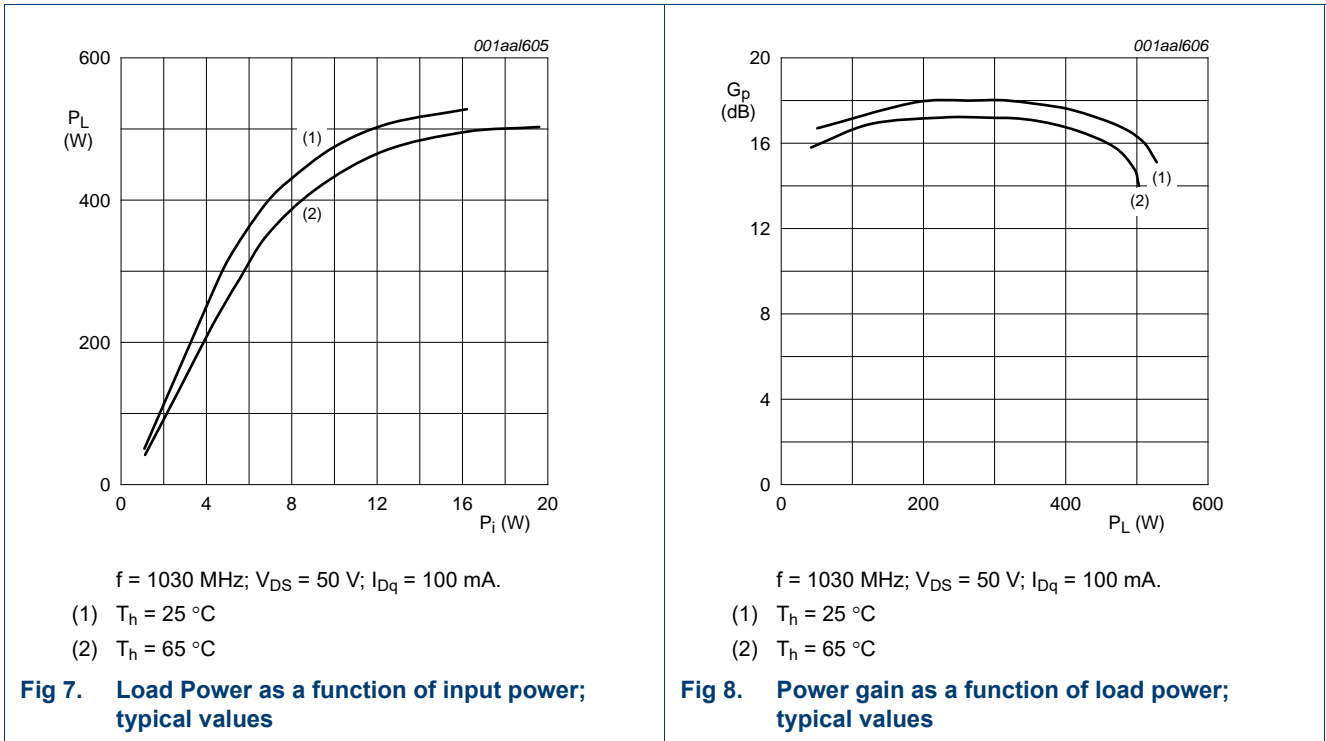
**Fig 5. Power gain and drain efficiency as function of frequency; typical values**



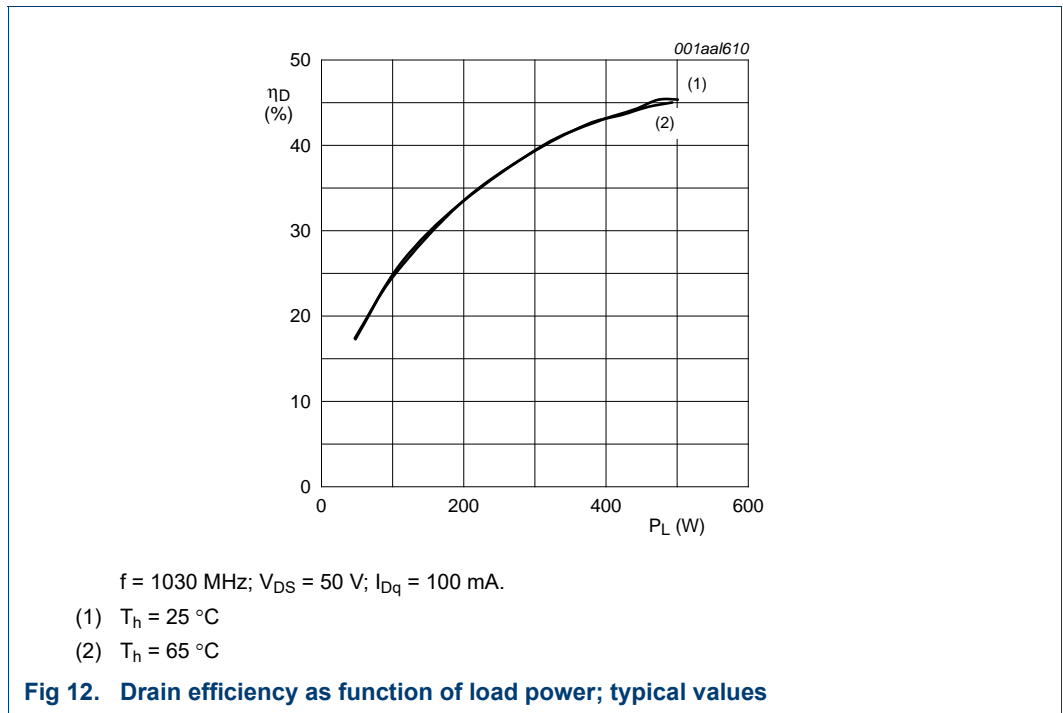
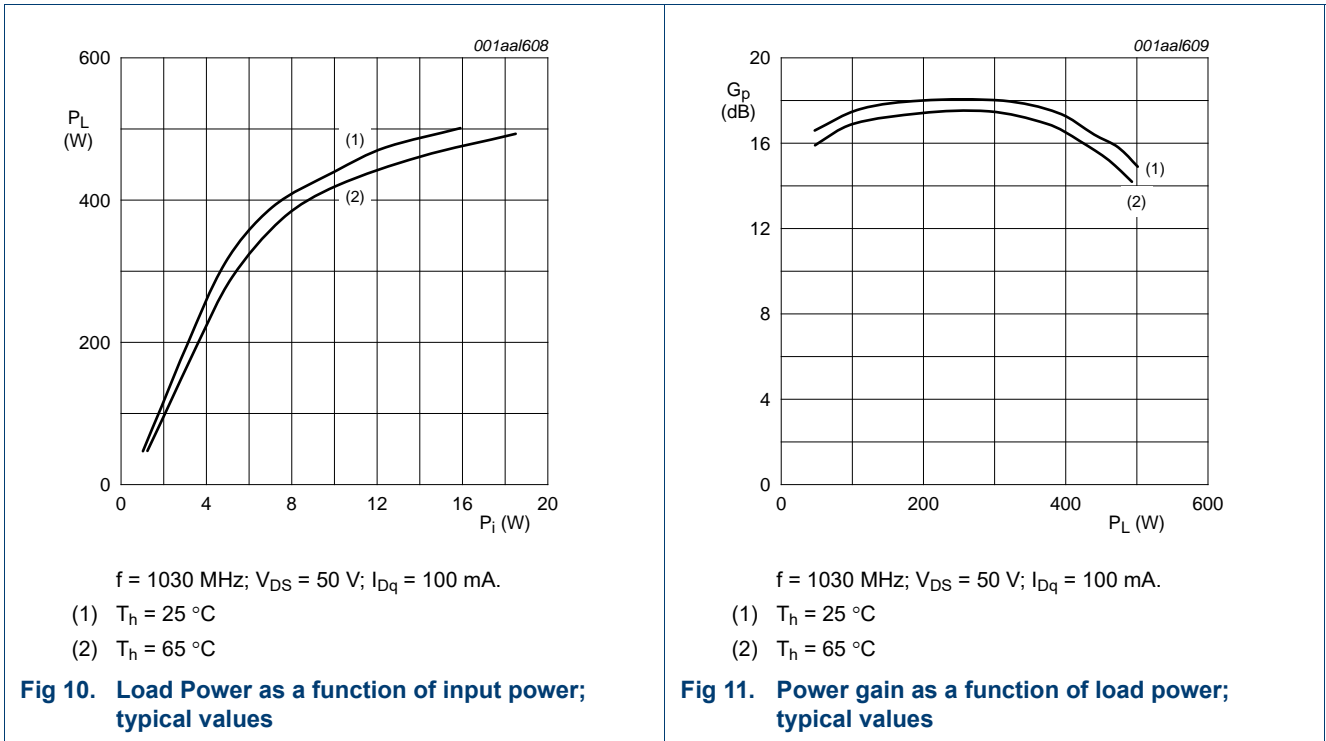
$P_L = 500 \text{ W}; V_{DS} = 50 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 128 \text{ }\mu\text{s}; \delta = 10 \text{ \%}$ .

**Fig 6. Input return loss as a function of frequency; typical values**

7.3 Curves measured under Mode-S ELM pulse-conditions

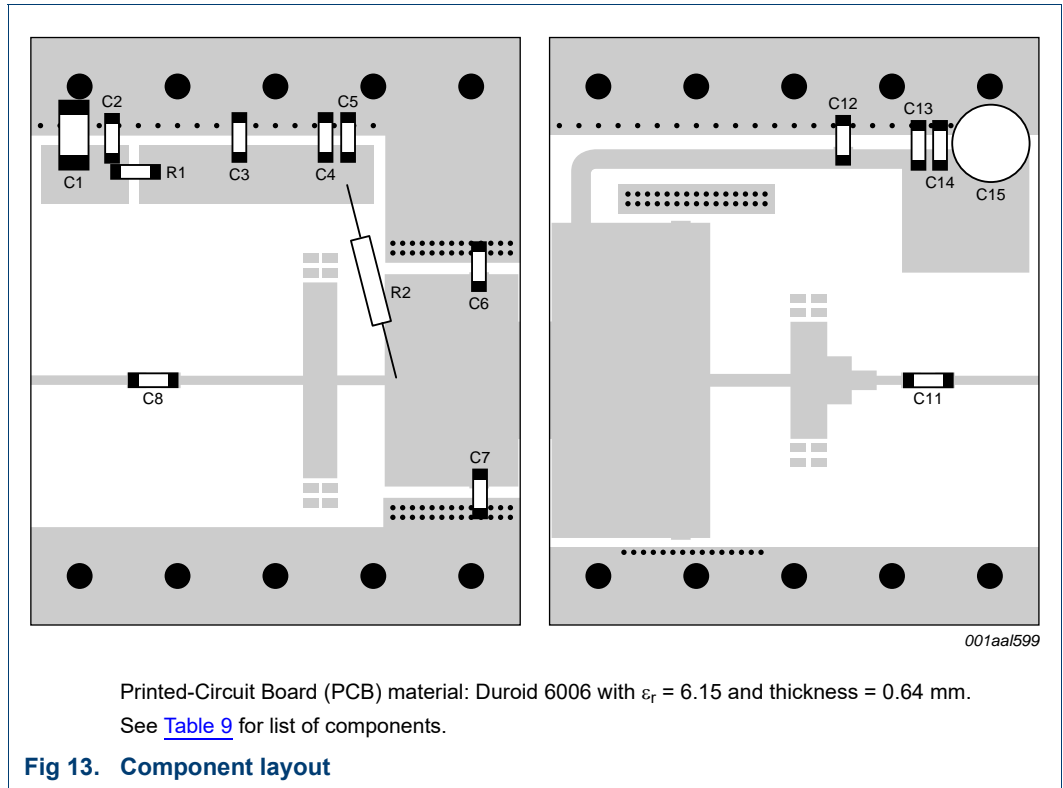


7.4 Curves measured under Mode-S interrogator pulse-conditions





8. Test information



**Table 9. List of components**  
See [Figure 13](#) for component layout.

Component	Description	Value	Remarks
C1, C3	multilayer ceramic chip capacitor	10 $\mu$ F; 35 V	
C2, C3, C14	multilayer ceramic chip capacitor	39 pF	[1]
C4, C13	multilayer ceramic chip capacitor	1 nF	[1]
C6, C7	multilayer ceramic chip capacitor	6.8 pF	[2]
C5, C8, C11, C12	multilayer ceramic chip capacitor	82 pF	[2]
C15	electrolytic capacitor	47 $\mu$ F; 63 V	
R1	SMD resistor	56 $\Omega$	SMD 0603
R2	metal film resistor	51 $\Omega$	

[1] American Technical Ceramics type 100B or capacitor of same quality.  
[2] American Technical Ceramics type 800B or capacitor of same quality.

9. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT634A

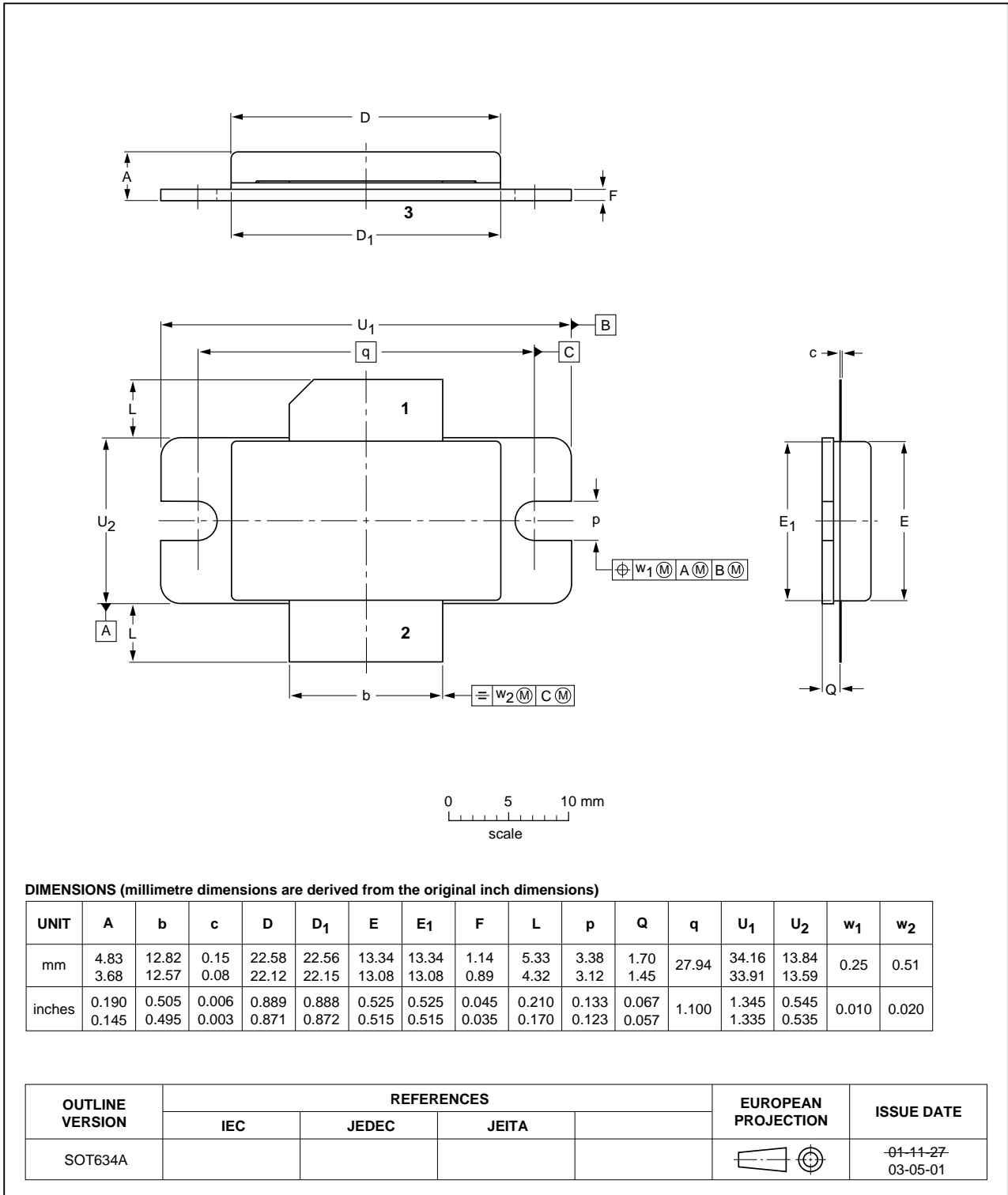


Fig 14. Package outline SOT634A

## 10. Abbreviations

Table 10. Abbreviations

Acronym	Description
DME	Distance Measuring Equipment
ELM	Extended Length Message
JTIDS	Joint Tactical Information Distribution System
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
Mode-S	Mode Select
RF	Radio Frequency
SMD	Surface Mounted Device
TACAN	TACTical Air Navigation
TCAS	Traffic Collision Avoidance System
VSWR	Voltage Standing-Wave Ratio

## 11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLA6H0912-500_5	20150901	Product data sheet	-	BLA6H0912-500_4
Modifications:	<ul style="list-style-type: none"> <li>The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>			
BLA6H0912-500_4	20100510	Product data sheet	-	BLA6H0912-500_3
BLA6H0912-500_3	20100330	Product data sheet	-	BLA6H0912-500_2
BLA6H0912-500_2	20100302	Product data sheet	-	BLA6H0912-500_1
BLA6H0912-500_1	20090305	Objective data sheet	-	-

## 12. Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[2] The term 'short data sheet' is explained in section "Definitions".

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